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ABANDONED

P

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10047858	01/14/2002	368	43	71	2818

**APPLICANTS: Batinovich Victor;
439

**CONTINUING DATA VERIFIED:

THIS APPLICATION IS A CIP OF 09/915,708 07/25/2001
WHICH IS A CIP OF 09/609,626 07/03/2000

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** FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed 35 USC 119 conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO
Verified and Acknowledged Examiner's initials		P1208
TITLE : Method and apparatus for vertically stacking and interconnecting ball grid array (BGA) electronic circuit devices		
U.S.DEP'T. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)		

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
Amount Due	Date Paid		Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
			Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner		
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